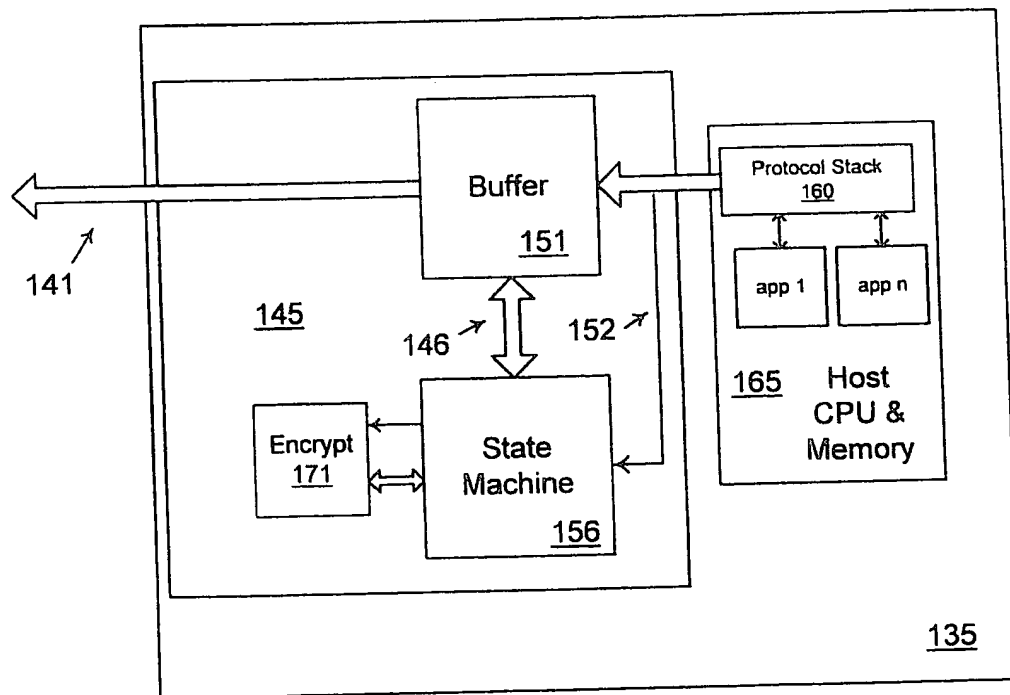
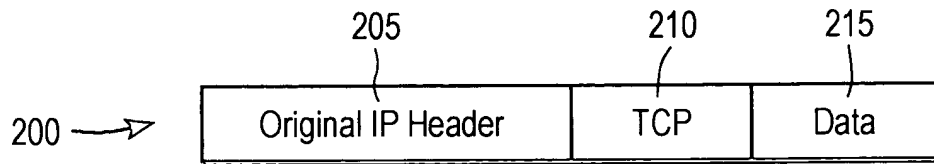


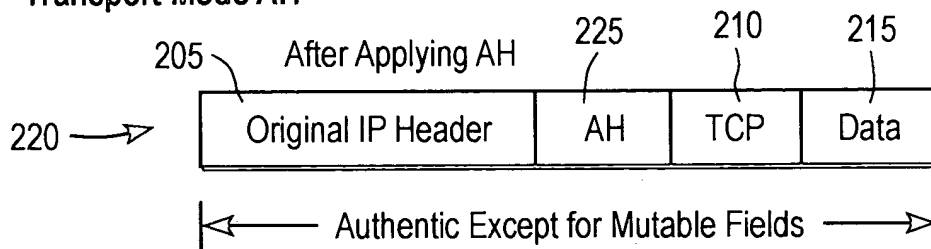
Fig. 1A



**Fig. 1B**



### Transport Mode AH



### Tunnel Mode AH

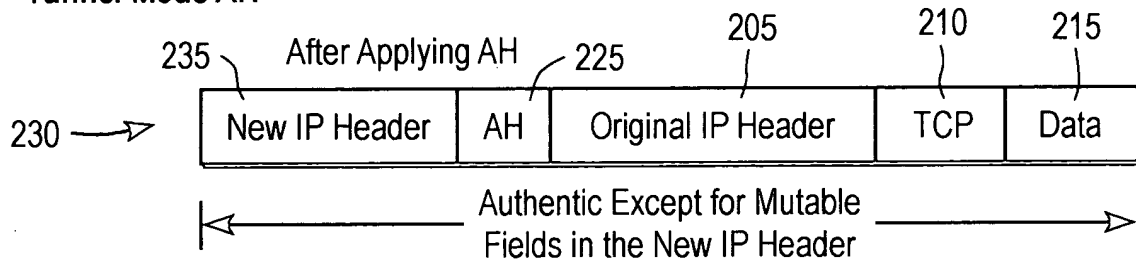
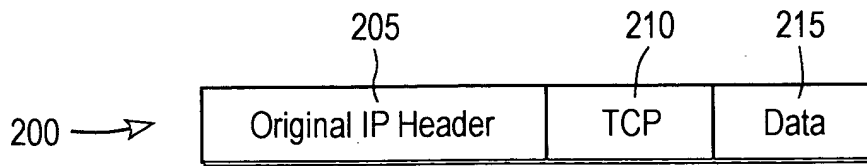
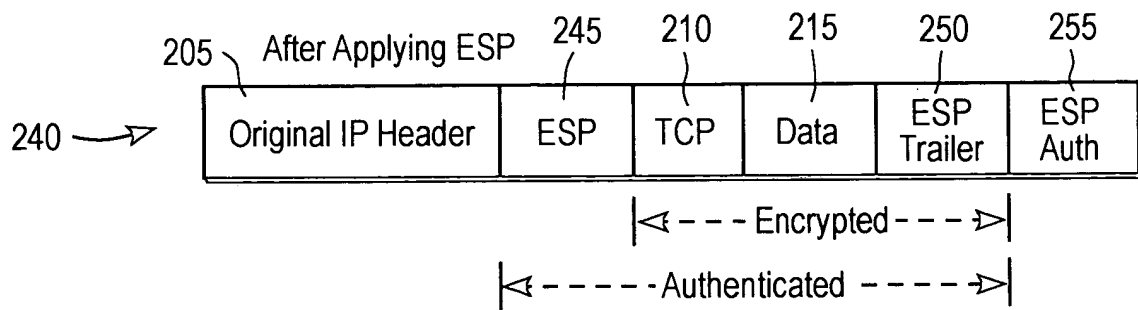


FIG. 2A



### Transport Mode ESP



### Tunnel Mode ESP

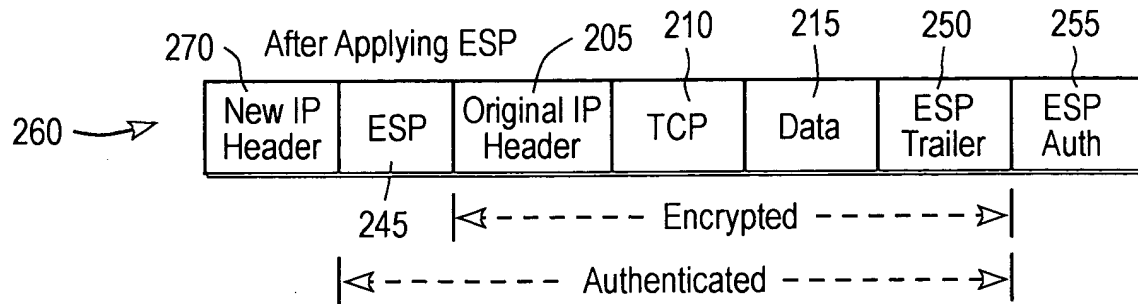
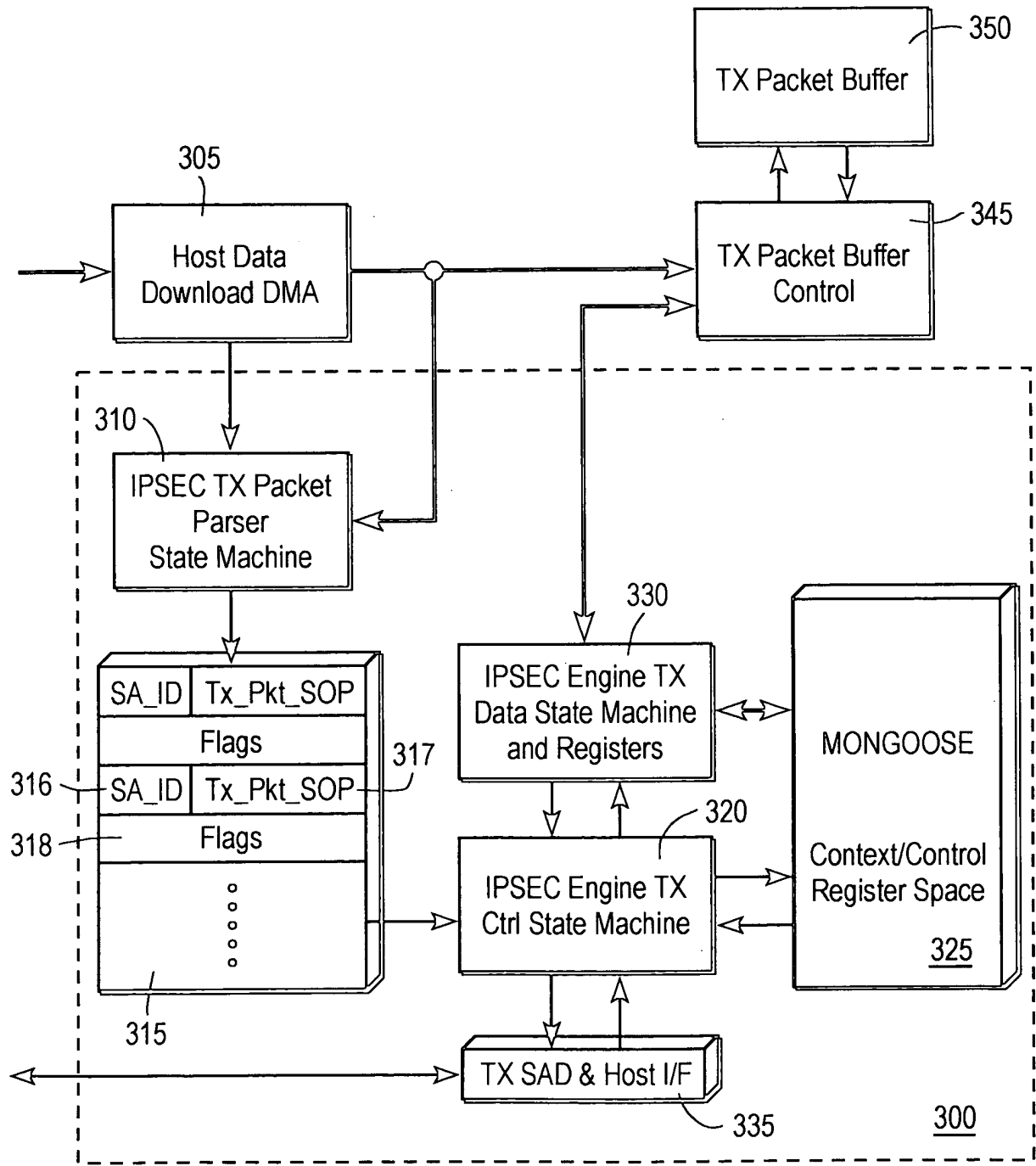
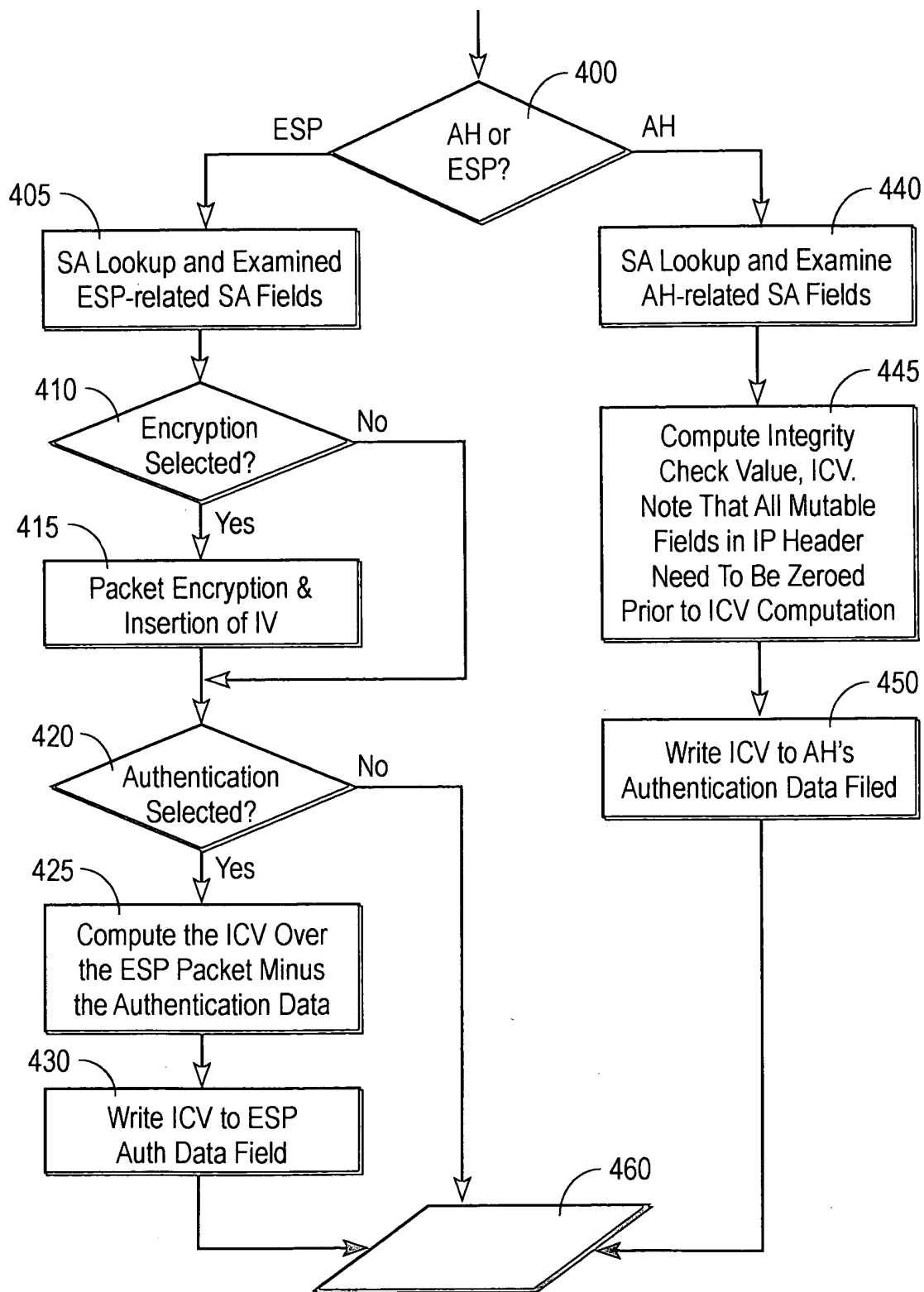


FIG. 2B



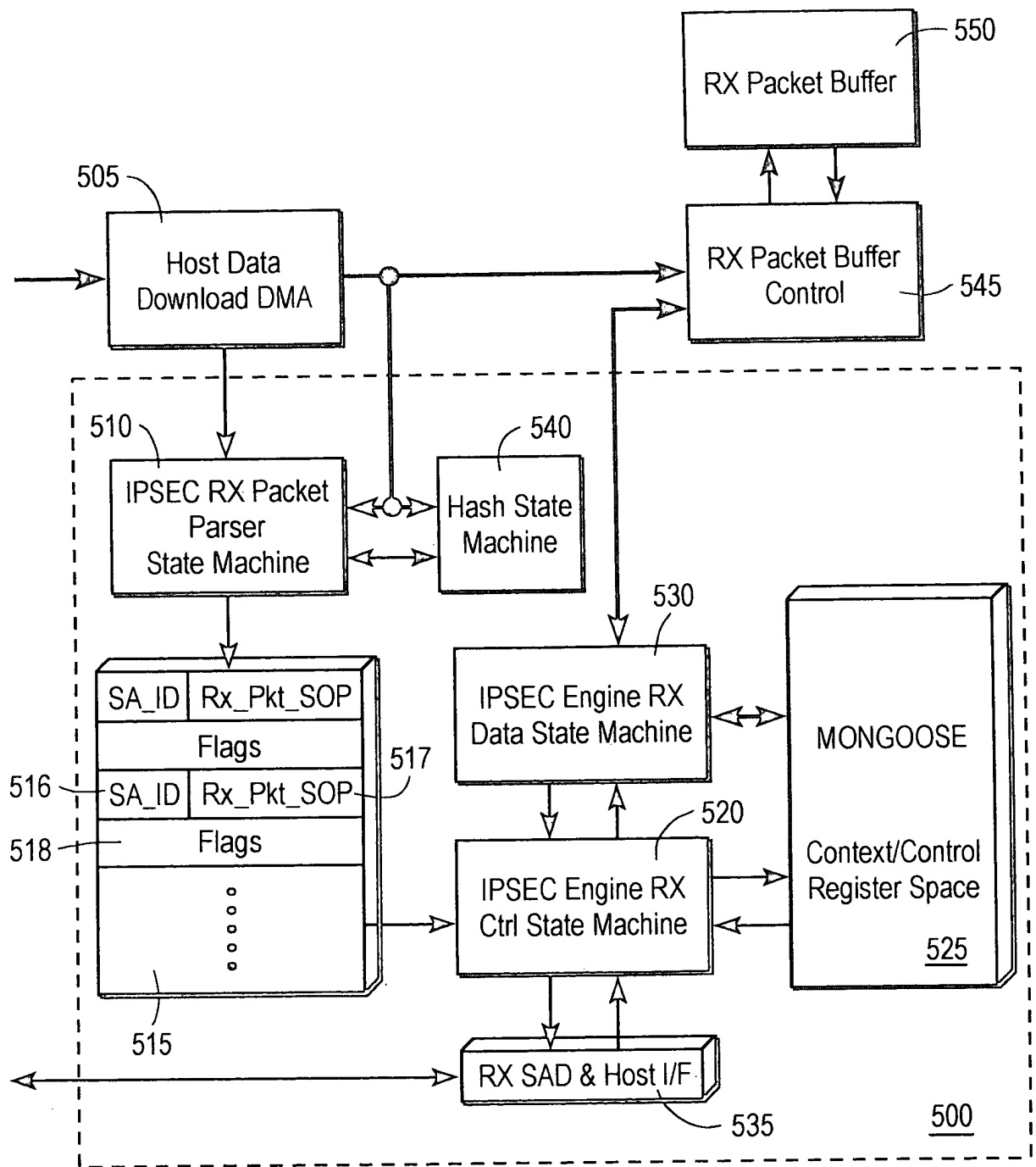
Hardware Architecture for Outbound Traffic

FIG. 3



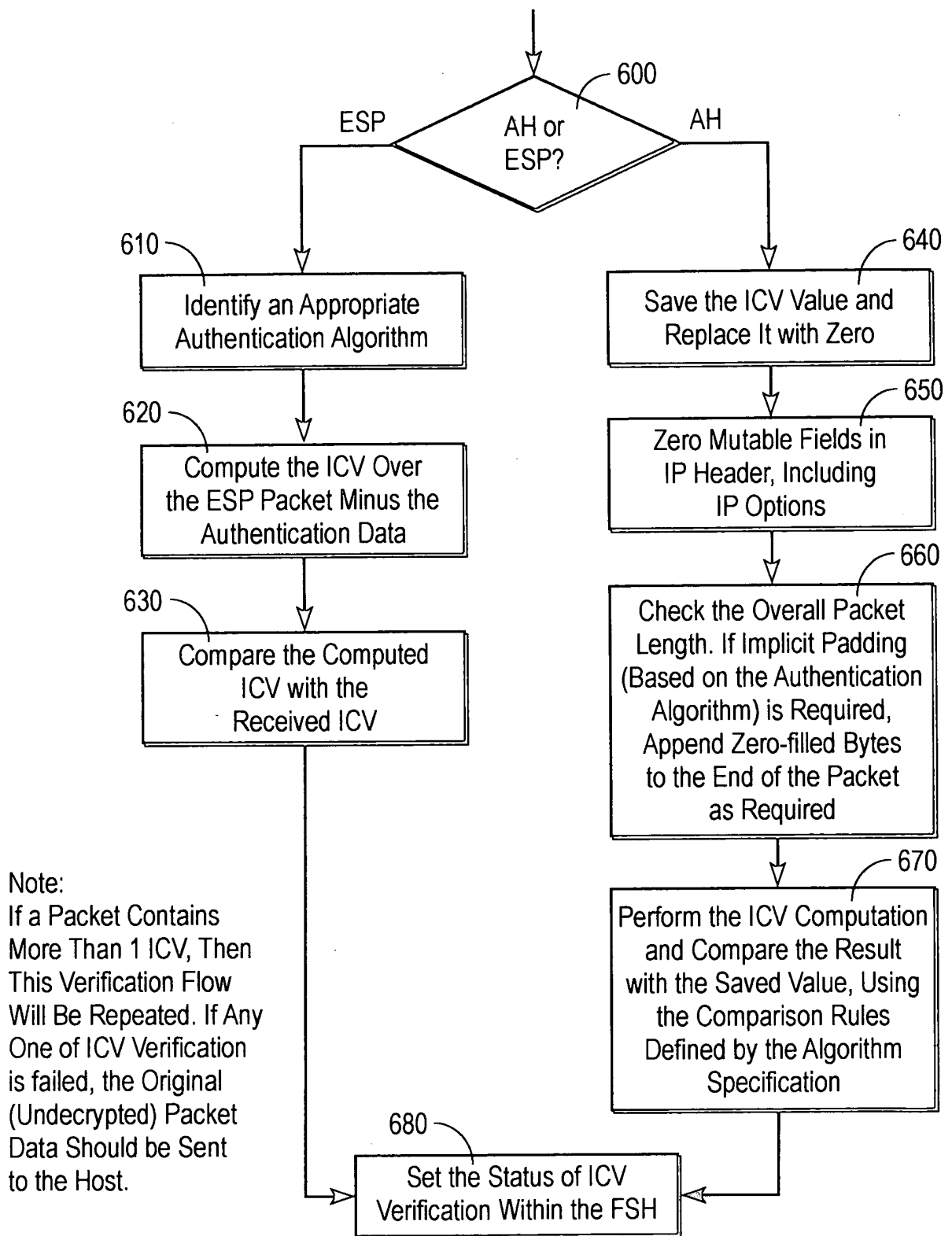
Outbound IP Traffic Processing Flow

FIG. 4



Hardware Architecture For Inbound Traffic

FIG. 5



Verification For Inbound Traffic

FIG. 6